

**July 27-29, 2022 ANSI Workshop on Global Supply Chain Security for Microelectronics Standardization
Speaker Biographies (Updated 7/25/2022)**



David Bergman
Vice President of Standards and Technology, IPC
Executive Director, Wiring Harness Manufacturer's Association

David W. Bergman is Vice President of Standards and Technology for IPC and has been with the organization for over 40 years and is currently responsible for IPC's Global Standardization Program, Standards Validation and Industry Solutions. David also serves as Executive Director of the Wiring Harness Manufacturer's Association (WHMA) an IPC affiliate.



Christine Bernat
Associate Director, Standards Facilitation
American National Standards Institute

Christine Bernat joined ANSI as Associate Director, Standards Facilitation in May 2022. In this role, she supports industry stakeholders, such as manufacturers, research institutions, government and standards development organizations, identify and conduct standards gap analyses through ANSI standards collaboratives and workshops.

Her most recent role prior to ANSI, Christine served as Director of Global Innovation and Policy at the General Aviation Manufacturers Association (GAMA), where she was responsible for supporting emerging technologies and innovation initiatives. At GAMA, she worked with key global aviation stakeholders, regulators and standards developers to enable introduction of new aircraft designs, propulsion technologies, flight capabilities to the market. During her tenure at GAMA, she also served as the Chairman of ASTM International F44 General Aviation Aircraft committee which develops standards for Part 23 airplanes, VTOL aircraft, electric propulsion and increasing automation.

Previously, Christine served as Director of Business Development and Manager of Technical Committee Operations, at ASTM International. In her 13 years at ASTM Christine supported organizational strategies for various industries including the aviation and aerospace, pharmaceutical, forensics, robotics, additive manufacturing, energy sectors. Her responsibilities included monitoring industry trends, exploring and launching new standards activities and related programs, establishing partnerships, and providing guidance regarding how standards can solve common challenges to bringing new products to market and support workforce development.

A native of New Jersey, Christine holds Bachelor of Arts degrees in Law and Justice and Spanish from Rowan University and a master's certificate in Translation from La Salle University. She is a former Ironman and is training for her private pilot's license.

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Jon Boyens
Deputy Chief of the Computer Security Division
Information Technology Laboratory
National Institute of Standards and Technology (NIST)

Jon Boyens is the Deputy Chief of the Computer Security Division in the Information Technology Laboratory at the National Institute of Standards and Technology (NIST). His responsibilities include Cybersecurity Research and Development at NIST and Cybersecurity Standards and Guidelines for Federal Agency Security Programs. He also leads NIST's Cyber Supply Chain Risk Management (C-SCRM) Program, helps develop and coordinate the Department of Commerce's cybersecurity policy among the Department's bureaus, and represents the Department in the Administration's interagency cybersecurity policy process. Boyens has worked on various White House-led initiatives, including those on trusted identities, botnets, the Cybersecurity Framework and Roadmap, telecommunications supply chain, software supply chain, and government-wide implementation of the Federal Acquisition Supply Chain Security Act, serving as NIST's principal to the Federal Acquisition Security Council.

Since 2010, Boyens has conducted research to identify, evaluate and develop technologies, tools, techniques, practices, and standards needed to enable organizations to manage supply chain risk. Building on this research, he led a team to develop and issue a set of foundational, standardized, repeatable, and feasible practices to help organizations manage cyber supply chain risks to their organizations and systems. These practices were released in 2015 as NIST Special Publication 800-161, Supply Chain Risk Management Practices for Federal Information Systems and Organizations. Continuing this line, Boyens has since released research and findings on criticality analysis and industry key practices for Cybersecurity SCRM. He is currently in the process of updating SP 800-161, working on software supply chain aspects of EO 14028, and leading the recently announced public-private partnership, the National Initiative for Improving Cybersecurity in Supply Chains.



Michelle Deane
Senior Director, Standards Facilitation
American National Standards Institute

Michelle Deane currently serves as the American National Standards Institute's senior director in the Standards Facilitation group supporting ANSI's Organizational Member Forum, which provides a forum for U.S. professional societies, trade associations, standards developers and academia to come together to discuss national and international standards and conformity assessment issues of interest. Ms. Deane also serves as the committee manager for several international standards development committees and works with U.S. interests on national, regional and international standardization in a variety of sectors including human resource management and healthcare.

Ms. Deane originally joined ANSI in 1993 and served for over seven years in the standards facilitation department. She left the Institute's full-time staff to pursue consulting opportunities, but continued to provide her expert services in several capacities on a contractual basis, including as senior program manager for the Healthcare Information Technology Standards Panel (HITSP), a cooperative partnership between the public and private sectors which achieved a widely accepted and useful set of standards that enabled and supported widespread interoperability among healthcare software applications. Ms. Deane rejoined ANSI as a director in 2012.

She received her Bachelor's degree in sociology and criminal justice from the State University of New York at Albany.

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Daniel DiMase
CEO
Aerocyonics, Inc., Aerocyonics
Imaging, LLC


Daniel DiMase is the CEO of Aerocyonics, Inc., Aerocyonics Imaging, LLC. and a Research Scientist at the University of Connecticut. Mr. DiMase has over 30 years of experience as an expert and recognized industry leader in Supply Chain Risk Management, Logistics, Counterfeit Avoidance & Detection, Cyber Physical Systems Security, and Hardware Assurance. Mr. DiMase's work at Aerocyonics is focused on delivering innovation with state-of-the-art products and services that serve markets in defense and commercial industry. Mr. DiMase's current areas of focus includes consulting services, product development and commercialization of nano-machining equipment, 3D imaging, failure analysis tools, reverse engineering instruments, material characterization, advanced traceability tools, software solutions, furthering the development of STEM education, and research and development of advanced polymorphic and neuromorphic architectures of microelectronic devices beyond Moore's Law addressing low-power, radiation hardened, and security by design.

Mr. DiMase's previous roles included establishing and growing an electronics distribution company from inception to a multi-million dollar global-logistics organization. In that organization, he established policies, procedures, systems, and training to import and export globally to over 37 countries in accordance with import, export, and International Traffic in Arms regulatory requirements. Daniel also managed and ran a global trade association that investigated, monitored, mediated and reported issues impacting the supply chain. In his role at Honeywell Aerospace, he was responsible for developing the appropriate course of action to address compliance to regulatory and customer requirements for their \$10B strategic business group with over 40,000 employees in over 125 domestic and international locations. He was responsible for implementing policy for counterfeit avoidance and detection across the Aerospace Strategic Business Group, addressing customer and regulatory compliance, implementation of industry standards, and overseeing suppliers and test laboratories.

Mr. DiMase served in leadership roles and provided significant contributions on numerous industry committees and working groups, including but not limited to the SAE G-32 Cyber Physical Systems Security Committee, SAE G-19 Counterfeit Electronics Parts subcommittees, SAE G-21 Counterfeit Materiel Committee, Global Semiconductor Alliance Trusted IoT Ecosystem Security (GSA TIES), The Aerospace Industries Association Counterfeit Parts Integrated Projects Team, and the TechAmerica Supply Chain Assurance Committee. He helped establish the Center for Hardware Assurance, Security, and Engineering (CHASE) center at the University of Connecticut, and served as the lead on the industry advisory board. The CHASE consortium has now transitioned into the Center for Hardware and Embedded Systems Security and Trust (CHEST) consortium. He has also been a member of the US Customs and Border Protection Advisory Committee on Commercial Operations Intellectual Property Rights Subcommittee and member of the Industry Advisory Group for Government-Industry Data Exchange Program (GIDEP). He is the Co-Chair of the NDIA Trust and Assurance Committee within the Electronics Division.

Mr. DiMase has a successful track record of building teams, inspiring and motivating staff, collaborating and leading cross-functional and cross-

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| | <p>industry groups with individuals from industry, government and academia. He is the recipient of the PBN 2022 Leaders & Achievers Award Program in Rhode Island. This award program recognizes senior leaders and achievers for their notable success and strong leadership both in their fields and to the region. He is also the recipient of the 2021 SAE Aerospace Engineering Leadership Award from SAE International in recognition of leadership and impactful contributions made within the Aerocyonics organization, the Aerospace community and industry at large. Other awards he has received includes the Dr. Desmond G. Newman Award for Supply Chain Excellence from the National Defense Industrial Association Manufacturing Division, the Arch T. Colwell Cooperative Engineering Metal from SAE International, and the Achievement Special Recognition Award at DMSMS by the U.S. Department of Defense in recognition of superior leadership and contributions in counterfeit prevention.</p> <p>Mr. DiMase has an Executive MBA from Northeastern University. He has a Six-Sigma Green Belt Certificate from Bryant University. He received his Bachelor of Science degree in Electrical Engineering from The University of Rhode Island.</p> |
|  <p>Gordon Gillerman Director, Standards Coordination Office National Institute of Standards and Technology (NIST)</p> | <p>Gordon Gillerman, Director, Standards Coordination Office at the National Institute of Standards and Technology (NIST) and Department of Commerce Standards Executive, leads NIST’s work in standards coordination and the National Voluntary Laboratory Accreditation Program. Gordon supports extensive standards development and advises federal agencies and other stakeholders on standards and conformity assessment policy. The Standards Coordination Office is the NIST standardization focal point for federal government, administers the NIST Standards Curricula Development Cooperative Agreement Program, operates the U.S. Inquiry Point for the World Trade Organization’s Technical Barriers to Trade Agreement, is the U.S. Designating Authority for Telecom Mutual Recognition Agreements, and is a key information source for US industry on standards related market access issues.</p> <p>Gordon has extensive standards experience across a wide range of critical issues including homeland security, safety, health, and protection of the environment. Gordon is an expert on conformity assessment systems design, an advisor to the U.S. Trade Representative on technical barriers to trade and related trade agreements, and has collaborated across the standards community to develop standards-based solutions for national priorities throughout his career. Gordon provided direct support in the drafting and negotiation of the Technical Barriers to Trade chapter of the USMCA.</p> <p>Prior experience includes leading government affairs for the largest U.S. product safety certification and standards development organization, Underwriters Laboratories (UL) in Washington, DC, and Staff Engineer for the medical device and information technology sectors at UL’s Northbrook, IL headquarters.</p> |

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Lori Gordon
Space Enterprise Integration
Initiatives Leader
Corporate Chief Engineer's
Office
The Aerospace Corporation

Lori W. Gordon leads Space Enterprise Integration initiatives in the Corporate Chief Engineer's Office at The Aerospace Corporation. In her role, she serves as an expert in national and homeland security, cybersecurity, and infrastructure risk and resilience. At Aerospace since 2018, she provides leadership across a range of critical infrastructure protection initiatives and internal technology strategy and investment. Gordon is also a partner with Aerospace's Center for Space Policy and Strategy (CSPS) and leads Aerospace's engagement with myriad collaborators, including the Space Information Sharing and Analysis Center and the National Security Institute (NSI). With more than 20 years of professional experience, Gordon has contributed to the development of national-level strategies, capabilities, and programs across homeland, intelligence, and civil agencies.

Gordon began her career focusing on energy efficiency and renewable energy policy supporting the Department of Energy before expanding her domain expertise across other critical infrastructure sectors, including information and communications technology and the defense industrial base. At The MITRE Corporation from 2005 to 2014 and at a Washington, D.C.-based strategy consultancy from 2014 to 2018, she led critical infrastructure security portfolios working with the government to reduce risk and accelerate adoption of emerging technologies.

Gordon continues to collaborate on policy and technical publications with the Atlantic Council, the NSI, and other think tanks on space industrial base; positioning, navigation, and timing; supply chain; and cybersecurity.

Gordon earned a bachelor's degree in geography cum laude from the University of Maryland, College Park, and a master's degree in public administration from the University of Massachusetts, Amherst. She is a certified Project Management Professional (PMP®).




Gordon has received numerous officer's awards and special recognition awards, including the Government Technology Services Coalition Citizen of Mission Award for her work in science, technology, engineering, and math (STEM) programming across the interagency.





Brett Hamilton
Deputy Principal Director,
Microelectronics
OUSD(R&E), Office of Deputy
CTO for Critical Technologies
U.S. Department of Defense

Mr. Brett Hamilton currently serves as the Deputy Principal Director, Microelectronics at OUSD(R&E) MOD. In that capacity he is responsible for programmatic and budgetary oversight of research development, testing, and evaluation (RDT&E) programs and analysis related to microelectronics. As an innovative thinker on leveraging commercial technology to accelerate DoD modernization and advanced capability development, Brett was instrumental in the development of the SHIP, RAMP and RAMP-C programs, and is responsible for the technical and financial execution.



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|  <p>Ife Hsu Senior Quality/Reliability Engineer Intel</p> | <p>Ife Hsu is senior quality/reliability engineer responsible for pathfinding, technology and standards development at Intel. At Intel, Ife has worked in lead roles in test engineering, board level/surface-mount process, package technology development, and stress/reliability tools/methods development. Presently, Ife is a chair in JEDEC JC14.1 (reliability test methods), a technical member of AEC (auto qualification and methods), member of USNC TAG of ISO TC22/SC32 WG8 (functional safety) and IEC TC91 (material/PCB processing), and chair in joint IPC/JEDEC committees. Under these engagements, he is responsible for collective development and harmonization of standards that introduce state-of-art technologies and methodologies to package/boards/enabling. Ife holds degrees in mathematics, computer science and industrial/systems engineering.</p> |
|  <p>Stephanie Lin Defense Microelectronics Cross Functional Team U.S. Department of Defense</p> | <p>Stephanie is currently supporting the Defense Microelectronics Cross-Functional Team in microelectronics-related policy and Congressional activities to include the development of trusted supply chain and operational security standards and DoD policies for microelectronics assurance. Prior to this role, she worked for the Strategic Technology Protection and Exploitation office within the Office of the Under Secretary of Defense for Research and Engineering where she drafted and coordinated on DoD policies across various program protection and related disciplines to include policies on Critical Program Information, Anti-Tamper, and Hardware Assurance. She also facilitated the establishment and execution of the Strategic Radiation-Hardened Electronics Council which is now the coordinating body for DoD on radiation-hardened microelectronics. She has a M.S. in Computer Engineering and a B.S. in Electrical and Computer Engineering.</p> |
|  <p>Alan Lucero Senior Quality & Reliability Engineering Manager / Director of International Standards Development Intel</p> | <p>Alan Lucero, started in Technology Development as a Semiconductor Reliability Engineer 26 years ago. His career has given him knowledge of all aspects of reliability and quality that spans from the transistor level to the system level. He managed the component use condition and reliability simulation group for 7 years after managing the package reliability group for 9 years. Semiconductor development and production has always required establishing quality metrics, metrologies and electronics systems to manage part data, unit integrity and quality.</p> <p>Currently, Alan directs reliability, quality and regulatory standardization at Intel and he has been a standards contributor for over 10 yrs. Alan is the chair of JEDEC JC14 semiconductor reliability committee for 4 years after having been the chair of the JC14.2 Wafer Level committee for 4 years. JEDEC serves as the USNC mirror committee for IEC TC47 where Alan has served as a convener for 6 years and in other roles as Tech/Assistant and expert for 8 years. Within IEC, Alan works in a liaison or coordination capacity with non-semiconductor committees in IEC such as TC107 Sys. Equip. for Avionics, TC91 Assembly, TC40 Discrete devices and TC56 Reliability-Maintainability.</p> |




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|  <p>Jim McCabe Senior Director, Standards Facilitation American National Standards Institute</p> | <p>Jim McCabe serves as senior director, standards facilitation, at the American National Standards Institute (ANSI), where he directs collaborative standardization activities for emerging technologies. Recent projects have included:</p> <ul style="list-style-type: none"> organizing meetings on standardization and the commercial space industry spearheading the development of a standardization roadmap for unmanned aircraft systems (drones) to facilitate their safe integration into the U.S. national airspace partnering with America Makes to develop a standardization roadmap for additive manufacturing (3D printing) <p>A member of the ANSI staff since 1995, Mr. McCabe has been recognized by America Makes with its Distinguished Collaborator Award, and by SES, the Society of Standards Professionals, with its Honorary Life Member award.</p> |
|  <p>Jeremy Muldavin Distinguished Member Technical Staff, Aerospace and Defense GlobalFoundries</p> | <p>Dr. Jeremy Muldavin is currently Distinguished Member of the Technical Staff for Aerospace and Defense at GlobalFoundries beginning in 2020. Prior to joining GlobalFoundries, Dr. Muldavin spent 19 years with MIT Lincoln Laboratory as a staff, and group leader researching microelectronics, imagers, embedded computing, open architecture, and autonomy, and was awarded the IEEE Microwave Theory and Techniques Society Outstanding Young Engineer award for his work. He received his BSE in engineering Physics, MSE and PhD in Electromagnetics from the University of Michigan. From 2016-2019, Dr. Muldavin was an IPA assignee as Director, Defense Software & Microelectronics Assurance and Microelectronics Assistant Director at the Department of Defense Office of the Assistant Secretary of Defense for Research & Engineering, where he established the Microelectronics Innovation for National Security and Economic Competitiveness (MINSEC) program and served as a representative to the DoD on the NSC sub-PCC for semiconductors.</p> |
|  <p>Paul Nixon Sr Principal Engineer II, Quality Assurance BAE Systems</p> | <p>Background:</p> <ul style="list-style-type: none"> Graduated with BS and Masters of Electrical Engineering from Rice University Over 40 years of service with BAE Systems and heritage companies Worked in Technology Characterization, Yield Management, Product Engineering, and Quality Assurance/Quality Engineering areas <p>Experience:</p> <ul style="list-style-type: none"> Lead Quality Engineer for the BAE Systems Manassas site, including role as the site lead for Quality Management System for AS9100, ISO 9001, and QML (Qualified Manufacturer's List, certification/qualification for DoD microelectronics); as well as the lead Quality Engineer for multiple microelectronic programs Member of JEDEC since 1998; currently chairperson of JEDEC JC-13 standards committee and member of SAE CE-12 and ESDA standards groups |

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|  | <p>Maria joined the IEEE-SA with a directive to educate and build collaborative working groups on the impact of emerging technologies in health and life science applications that will lead to security, protection, and sustainable equitable access to quality care for all. As leader of the IEEE Healthcare and Life Science Global Practice one of the greater concentrations of work with volunteers is on driving responsible adoption of viable blockchain and IoT applications for securing and auditing data and product supply chains for medical device, bio/pharmaceutical, health, agriculture, circular economy and more.</p> <p>Maria founded the information company, DisruptiveRx™, a platform that brings pragmatic focus on breakthrough technology innovations in the pharmaceutical and health domain that would lead to enhanced patient care, safety and optimizing of therapeutic development.</p> <p>Maria currently holds an MBA from the Rutgers Graduate School of Business and a BA and BS from Rutgers College at Rutgers University, the State University of New Jersey. She is an accomplished leader, public speaker, and writer having presented at various industry events and published blogs and articles in trade publications.</p> |
| <p>Maria Palombini Global Practice Leader, Healthcare & Life Sciences IEEE- Standards Association</p> | <p>Daniel Radack is an Assistant Director in the Information Technology and Systems Division at the Institute for Defense Analyses (IDA) where he analyzes challenges related to microelectronics for the US Government and develops solutions and courses of action. From 1997 to 2006, he was with DARPA as a Program Manager in the Microsystems Technology Office (MTO) where he started and managed a portfolio of R&D programs that advanced high performance semiconductor technologies and he conceived the first efforts in trusted microelectronics research, including the DoD's trusted multi-project wafer program. Research under those DARPA programs led to foundational advances to the state of art in transistor structures, Silicon-Germanium, Silicon-on-Insulator, Silicon-Carbide power devices, radiation hardening by design, platform-based design, multi-chip packaging, thermal management, and heterogeneous integration/3D integration. In 1997, he conceived and initiated the Focus Center Research Program, pioneering a joint funding and management model among the Federal Government (DoD/DARPA), State and Local Government, and the US Semiconductor Industry and suppliers to manage research centers at US academic institutions; a very successful model of joint research that continues today 25 years later. Prior in the early 1990's he assisted DARPA in technical management of SEMATECH and other dual-use programs to accelerate industrial competitiveness. Before that, he worked in the defense electronics industry and for NIST where he studied silicon and GaAs dynamic test circuits and semiconductor metrology. He has a BS, MS, and Ph.D. in Electrical Engineering from the University of Maryland. He is a Fellow of the IEEE.</p> |
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| <p>Daniel Radack, PhD Assistant Director Information Technology and Systems Division Institute for Defense Analyses (IDA)</p> | |

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|  | <p>Mike Regan leads the activities of the Telecommunication Industry Association’s (TIA) QuEST Forum with a focus on business performance improvement standards and associated activities. Prior to joining TIA, Mike completed a successful 30+ year career as a senior engineering leader responsible for the delivery of complex communications and networking products deployed in the business-critical production networks of premier public service providers, global cloud platforms, large enterprises and customer engagement centers.</p> <p>Mike leverages his personal experiences in progressing the initiatives of the TIA QuEST Forum by working with Forum participants, network operators, government agencies and industry peers towards the development and adoption of new standards for the ICT industry with an emphasis on product quality, secure software development, and supply chain security.</p> |
| <p>Mike Regan Vice President of Business Performance Telecommunication Industry Association’s (TIA) QuEST Forum</p> | |
|  | <p>Christine Rink is the Associate Deputy Director for Microelectronics Assurance Standards for OUSD (R&E) Modernization. She has over twenty years of experience in the design and development of microelectronics and has worked in hardware assurance for more than a decade. Prior to joining the OUSD (R&E) team, Christine served as the ASIC lead for a high-volume, ASIC focused DoD program. She has successfully enabled state of the art microelectronics access for programs by developing approaches to reconcile program technology needs with DoD trust policy and export control regulation.</p> <p>Christine holds a BS in biomedical and electrical engineering and a MS in electrical engineering from the University of Southern California.</p> |
| <p>Christine Rink Associate Deputy Director for Microelectronics Assurance Standards OUSD (R&E) Modernization U.S. Department of Defense</p> | |
|  | <p>Mary Saunders leads ANSI’s efforts to advocate greater use of voluntary consensus standards and conformance programs by government agencies and broader participation by agency personnel in standards development. She works with ANSI members to create standardization-related outreach programs to legislators and to increase understanding of the private-sector standards community among agencies involved in trade and commerce issues. Mary is a key player at ANSI in fostering understanding among opinion leaders of the major role standards and conformance play in the international and domestic marketplace.</p> <p>Ms. Saunders has extensive federal government executive-level experience and served in a variety of positions within the Department of Commerce prior to joining the ANSI staff, most recently as the Associate Director for Management Resources at the National Institute of Standards and Technology (NIST). As Director of NIST’s Standards Coordination Office, she represented NIST and its significant interests in the standards and conformity assessment community, advising the NIST Director and other officials throughout the Administration on policy and strategy as they relate to the federal government’s role in standardization.</p> |
| <p>Mary Saunders Vice President for Government Relations and Public Policy American National Standards Institute</p> | |

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|  <p>Roger Smith Defense Microelectronics Cross Functional Team – Navy Lead</p> | <p>Mr. Smith entered public service at Naval Surface Warfare Center Crane Division in October 1987 serving in both project technical and leadership positions throughout his tenure. In July of 2015 Mr. Smith accepted his current assignment as the DoD Executive Agent for Printed Circuit Board and Interconnect Technologies - Navy Technical Lead. In this role, he supports initiatives to oversee issues related to Trust, Technology Development, Supply Chain Management, and Knowledge and Capability Sustainment. As a collateral and complementary duty, Mr. Smith also currently serves as the Naval Sea Systems Command, Technical Warrant Holder for Electronics.</p> <p>Mr. Smith holds a Bachelor’s Degree in Mechanical Engineering from Purdue University, a Master’s Degree in Public Affairs from the Indiana University School of Public and Environmental Affairs, and is Level III certified in the Defense Acquisition Workforce - Systems, Production, Research and Engineering career field.</p> |
|  <p>Kanitra Tyler Information & Communications Technology (ICT) Supply Chain Risk Management (SCRM) Service Element Lead National Aeronautics and Space Administration (NASA)</p> | <p>Kanitra Tyler is an expert in network security and cybersecurity policy, and is highly sought after for her expansive depth of knowledge. She is an informed voice and valued contributor at any table at which she takes a seat. With an unmatched reputation for transforming organizations through process and people, Kanitra is a passionate advocate for collaboration, and, in every role she assumes, proves that partnerships are what move missions forward.</p> <p>Over the course of her constantly evolving career, Kanitra has held a number of roles, including the GSFC Deputy Chief Information Security Officer and Program Manager of the inaugural IT Security Change Management team. In 2018, she was tapped to lead the Agency’s Information and Communications Technology (ICT) Supply Chain Risk Management (SCRM) Service. She has brought her full breadth of experience to the position, including a proven track record for successfully leading dynamic teams. Kanitra has set out to mature the Agency’s capability to make near, real-time, risk-based decisions, and to create a culture of collaboration, efficiency and compliance.</p> <p>Kanitra holds a Masters in Information Architecture, a Masters in Network Security, and several industry certifications, including a CISSP, CAP, CEH, NSA IAM/IEM, CHFI, CECES and ITIL v3.</p> |
|  <p>John C. Zolper, Sr. PhD Defense Technology Strategy Principal Engineering Fellow Raytheon Technologies</p> | <p>John Zolper leads Defense Technology Strategy in Technology and Global Engineering (T&GE), Defense Technologies. He contributes to defining and executing a technology strategy for the Defense side of the company in partnership with the commercial aerospace side. He is a member of the Technology Council and Research Council. Prior to the merger with United Technologies in 2020, he was the Vice President of Technology and Innovation in Corporate Technology and Research at Raytheon Company, which he joined in 2007.</p> <p>Previously, Zolper served in roles of increasing responsibility at the Defense Advanced Research Projects Agency (DARPA) from 2001 to 2007. As the director of the Microsystems Technology Office (MTO), he was responsible for the strategic planning and execution of a portfolio of more than 75 research programs with an annual budget of more than \$400 million. Before his appointment as director of MTO, Zolper was the chief</p> |

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| | <p>scientist/deputy director and a program manager, where he oversaw DARPA's multiyear \$250 million Wide Bandgap Semiconductor program and the Technology for Frequency Agile Digitally Synthesized Transmitters program.</p> <p>Before joining DARPA, Zolper was a program officer at the Office of Naval Research from 1997 to 2001 where he managed a portfolio of basic and applied research in advanced electronics. From 1989 to 1997, Zolper was a principal member of the technical staff at Sandia National Laboratories.</p> <p>Zolper holds a bachelor's degree in physics from Gettysburg College, and he earned a master's degree and doctorate in electrical engineering from the University of Delaware. From 1988 to 1989, Zolper performed his post-doctoral research on high efficiency silicon solar cells at the University of New South Wales in Sydney, Australia.</p> <p>Zolper was awarded the Exceptional Public Service Award from the Office of the Secretary of Defense in 2007. Zolper is also a fellow of the IEEE (Institute of Electrical and Electronics Engineers) and an associate fellow of the AIAA (American Institute of Aeronautics and Astronautics). He served on the Air Force Scientific Advisory Board from 2010 to 2014 for which he was awarded the Department of the Air Force, Commander's Public Service Award.</p> |
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